

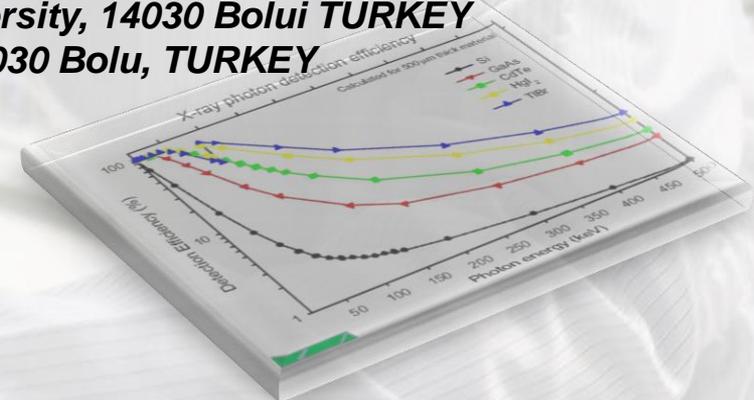
# Effects of annealing temperature on the crystallographic, morphological and electrical characteristics of E-Beam deposited Al/Eu<sub>2</sub>O<sub>3</sub>/n-Si (MOS) capacitors

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# Introduction

- Rare earth oxides (REO's) play an important role in semiconductor technology.
- Rare Earth Oxides (REOs) gained more attention for the improvement of the MOS devices.
- Europium oxide ( $\text{Eu}_2\text{O}_3$ ) is one of the REO and it has been used in many applications such as optoelectronics, telecommunications, microelectronics and optical devices.(Kumar et al., 2015)
- $\text{Eu}_2\text{O}_3$ ;
  - ✓ high dielectric constant ( $k = 14$ ) (Kumar et al., 2015),
  - ✓ large energy band gap (4.4 eV),(Kumar et al., 2016; Petit et al., 2005; Singh & Shivashankar, 2005).
  - ✓ high chemical durability and thermal stability (Loureno et al., 2011).



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# Experimental Details



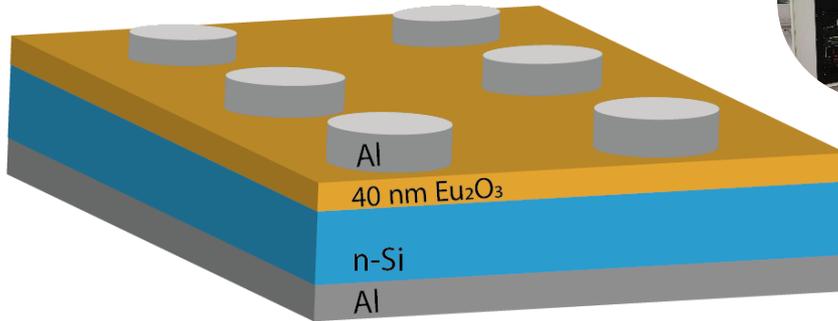
500  $\mu\text{m}$  thickness, n-type (100) silicon substrate with a resistivity of 2-4  $\Omega\text{ cm}$



The standard Radio Corporation America (RCA) cleaning procedure



$\text{Eu}_2\text{O}_3$  thin films with thickness of 40 nm were deposited at 150  $^\circ\text{C}$  substrate temperature by using electron beam (e-Beam) evaporation.



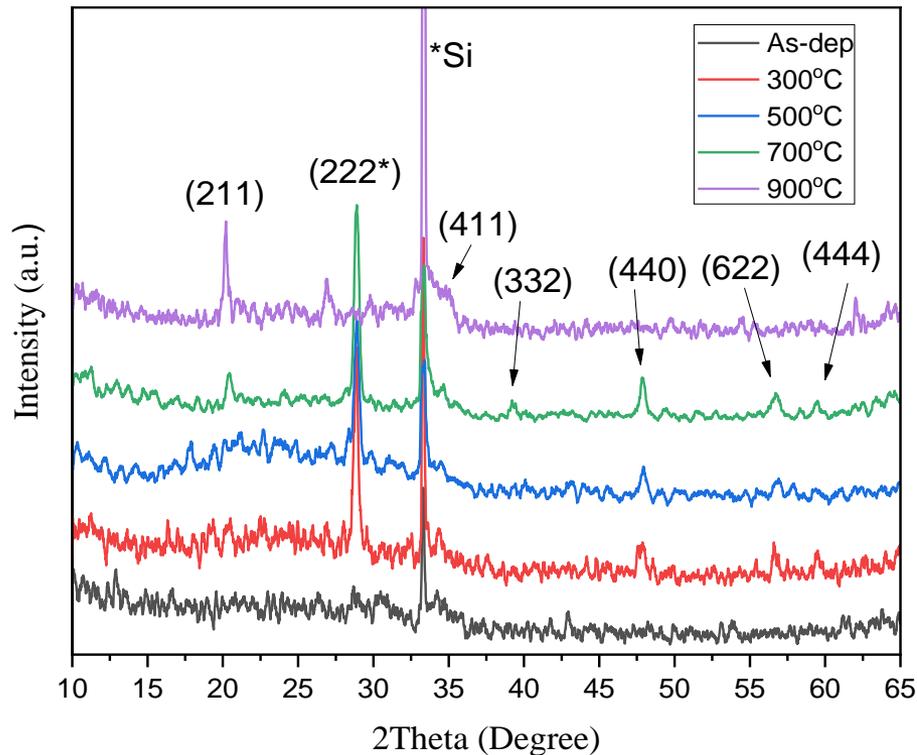
Samples annealed at different temperatures such as 300  $^\circ\text{C}$ , 500  $^\circ\text{C}$ , 700  $^\circ\text{C}$ , 900 $^\circ\text{C}$  for 40 min at  $\text{N}_2$  ambient.



To Form Aluminium front electrodes using a shadow mask having circular dots of 1.5 mm diameter and to deposit Aluminium onto the whole back surface of the wafer by DC sputter.

- Types of chemical bonding in  $\text{Eu}_2\text{O}_3$  were determined by a Perkin Elmer Spectrum Two FTIR-ATR spectrophotometer.
- Crystallinity of  $\text{Eu}_2\text{O}_3$  was analyzed by a Rigaku Multiflex diffractometer employing  $\text{CuK}\alpha$  radiation.
- The capacitance–voltage (C–V) and conductance–voltage ( $G/\omega$ –V) measurements for fabricated Al/ $\text{Eu}_2\text{O}_3$ /n-Si (MOS) capacitor were performed at 1MHz at room temperature.

# XRD Results



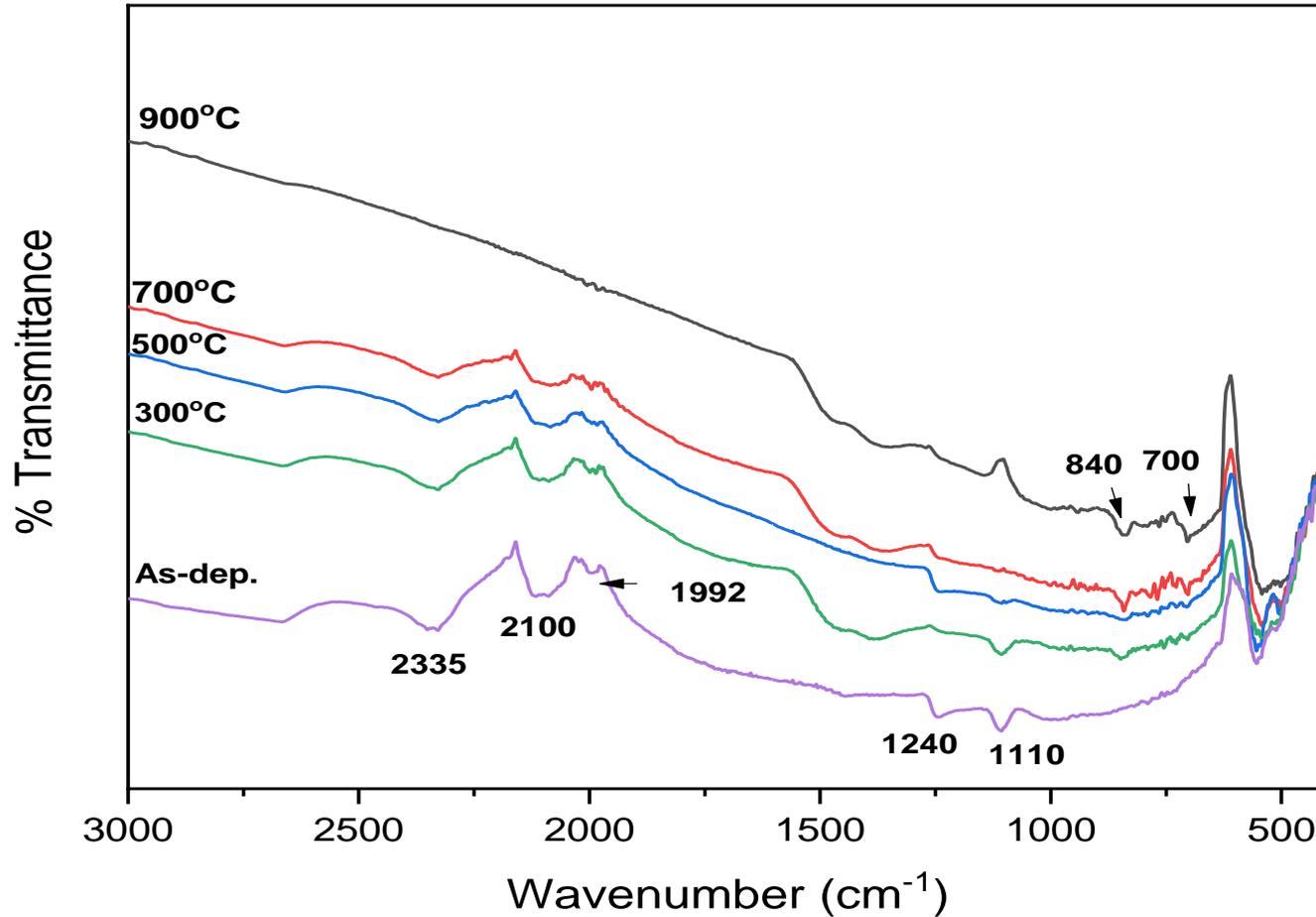
**Figure 1:** The XRD pattern of the  $\text{Eu}_2\text{O}_3/\text{Si}$  structure as- -deposited and annealed at 300 °C, 500 °C, 700 °C, 900 °C.

- The diffraction patterns of the annealed thin films can be indexed to the *cubic* phase, which is consistent with the values in the standard card ( JSPDS no. 34-0392, quality «\*», a value 10.86 nm).
- The values of grain size and crystallinity of the films increase with the increasing annealing temperatures, except 900°C annealed sample

**Table 1:** Grain size (nm) and Crystallinity (%) of deposited  $\text{Eu}_2\text{O}_3/\text{Si}$  depend on annealing.

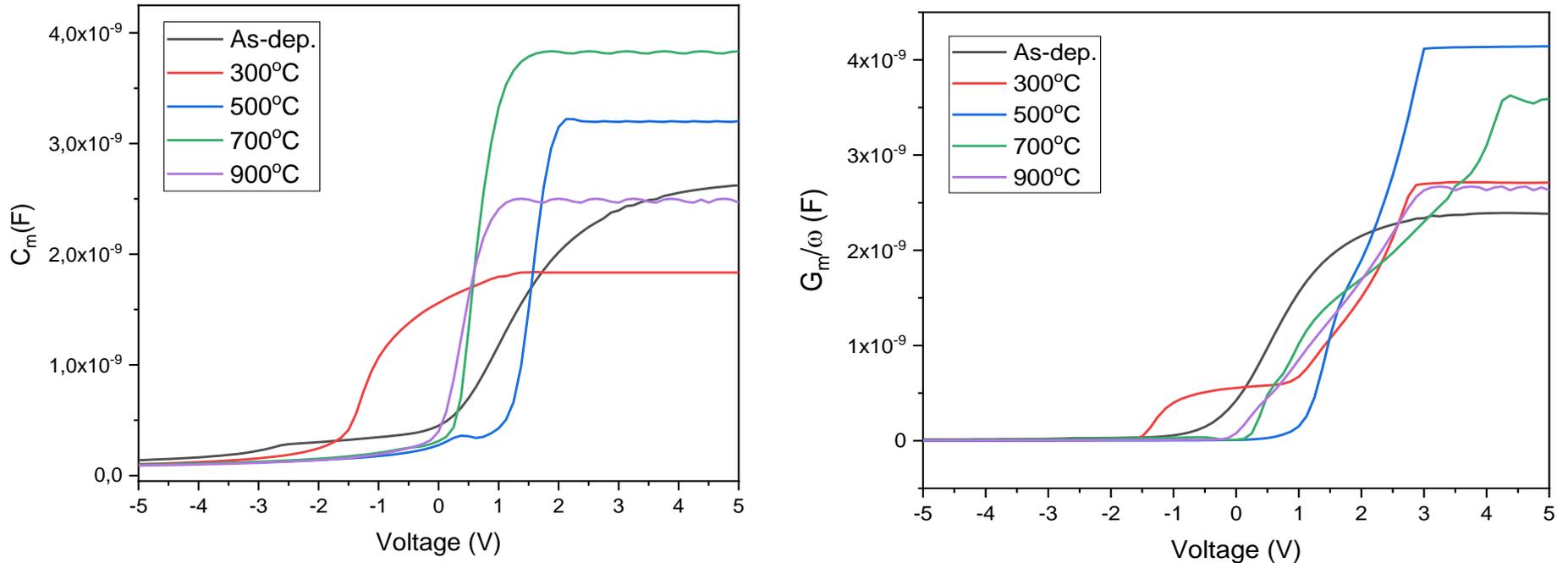
Annealing Temp.	Grain Size (nm)	Crystallinity (%)
300°C	23.69	15.70
500°C	24.28	15.81
700°C	26.45	29.39
900°C	12.15	18.01

# FTIR Results



**Figure 2:** FTIR spectra of Eu<sub>2</sub>O<sub>3</sub>/Si thin films at different annealing temperatures..

# Electrical Characteristics Results



**Figure 3:** The electrical C-V and  $G/\omega$ -V characteristics of Al/Eu<sub>2</sub>O<sub>3</sub>/Si/Al MOS capacitors.

# Conclusion

- In this study,  $\text{Eu}_2\text{O}_3$  MOS capacitors have been fabricated by using the Electron Beam Evaporation (E-Beam) technique and the effects of different annealing temperatures on them have been investigated.
- The crystallinity of  $\text{Eu}_2\text{O}_3$  thin film is sensitive to the annealing, while the grain size of films slightly increases with annealing temperature increase.
- The result of ATR-FTIR measurements shows that the Eu-O bond has been found on thin film structures at  $840\text{ cm}^{-1}$ ,  $700\text{ cm}^{-1}$  wavenumbers.
- The graphs of The C-V and G/w-V analyses show an increasing trend with the increasing annealing temperature.
- The results show that  $\text{Eu}_2\text{O}_3$  rare earth materials can be a good candidate for microelectronic applications.

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